

Amendments to the Specification:

Please replace the paragraph beginning at page 2, line 20 as with the following amended paragraph:

In the case of conducting color display, the respective emissions of R, G and B are controlled using, for example, three adjacent pixels represented by the dotted frame 520 in Fig. ~~5(A)~~5(B), and multicolor display is conducted by mixing these colors. In other words, three pixels are required for a 1-bit display.

Please replace the paragraph beginning at page 9, line 14 as with the following amended paragraph:

The first to third current supply lines 103 to 105 are connected to a control circuit 1401 of Fig. 14. The control circuit 1401 switches the connections of switches 1402 to 1404 respectively, whereby it controls the potentials of the current supply lines 103 to 105 to be  $V_A$  or  $V_E$   $V_B$ . Thus, it conducts field sequential driving. The configuration of the control circuit is not limited to FIG. 14. In Fig. 14, the control circuit has a configuration using the two potentials of  $V_A$  and  $V_E$ - $V_B$ , but the control circuit may also have a configuration that switches three or more potentials.

Please replace the paragraph beginning at page 17, line 25 as with the following amended paragraph:

In the example of FIG. 8, the gate signal line drive circuit includes, similar to the source signal line drive circuit, a shift register 802 using a plurality of flip-flips 801, NANDs 803, level shifters 804 and buffers 805. Here also, similar to the case of the source signal line drive circuit, the NANDs ~~802~~803, the level shifters ~~803~~804 and the buffers ~~804~~805 may be disposed as necessary.

Please replace the paragraph beginning at page 20, line 21 as with the following amended paragraph:

By repeating this operation with respect to the first to third emission colors, multicolor ~~expression~~display can be realized by the afterimage effect with respect to the viewer.

Please replace the paragraph beginning at page 22, line 24 as with the following amended paragraph:

A base film 3002 is formed on an insulating substrate 3001 (a flexible substrate is also possible) such as quartz, non-alkaline glass or plastic, and an active element group including first to third TFTs for driving 3004 to 4006~~3006~~ is formed thereon. 3003 is a gate insulating film of the TFTs 3004 to 3006. Moreover, first and second interlayer insulating films 3007 and 3008 are formed, and after contact holes are formed in the insulating films, wiring (not shown) and first pixel electrodes 3009 are formed.